Kafrelsheikh University Faculty of Engineering Department of Electrical Engineering (Computers and Systems)

Solve the following questions:-



Time allowed: 3 Hours Full mark: 70 Marks

(20 Marks)

Academic year: 2020-2021 Year: 3rd year

Subject: Digital Communications

Question One:		(20 Marks)
Pat (3) or (x) then correct the	e false one	
1- Flat top sampling is simi	lar to impulse sampling exc	cept that the impulse train is replaced by
pulse train signal.		
2. Line coding is the process	for converting digital signal	into digital data.
3- To demodulate a PWM of	or PPM signal, it is only requ	uired to pass the signal through low pass
filter (LPF) and we can go	of the message signal in the o	output.
4- The quantizing error cons	sists of the difference between	en the analog signal at the sampler input
and the output of the quat	itizer.	·
5- In AM, modulation inde	x is a dimension less factor	r, which measure the depth or degree of
modulation		·
6- It is considered a perfect	sampling which used to mo	dulate a massage signal of frequency = 1
kHz by PAM system us	ing a pulse generator of sa	mple time =0.25 ms and the number of
emontos per evele. 2 sam	oles.	
7- In synchronous transmiss	sion, we send one start bit (0) at the beginning and one or more stop
Fire (1) at the end of each	byte.	
8- In AM the total power of	the modulated signal is only	the power in the two sidebands.
0. There may be nogan below	cen each byte in synchronous	s transmission.
to The a law algorithm is a	companding algorithm. Its r	purpose is to reduce the dynamic range of
an andio signal.	· · · · · · · · · · · · · · · · · · ·	
		(40 1.)
Question Two:	•	(10 marks)
Choose the correct answer:	·	
Choose the correct answer:	smission, we send bits one	
Choose the correct answer:		after another without start / stop bits or
Choose the correct answer: 1- In	smission, we send bits one (b) Asynchronous	
Choose the correct answer: 1- In	(b) Asynchronous	after another without start / stop bits or (c) both of them
Choose the correct answer: 1- In tran gaps. (a) Synchronous	(b) Asynchronous	after another without start / stop bits or (c) both of them he analog to digital converter,
Choose the correct answer: 1- In	(b) Asynchronous nniques used to implement the inique but requires more hard	after another without start / stop bits or (c) both of them he analog to digital converter,
Choose the correct answer: 1- In tran gaps. (a) Synchronous	(b) Asynchronous nniques used to implement the inique but requires more hard	after another without start / stop bits or (c) both of them he analog to digital converter,
Choose the correct answer: 1- In	(b) Asynchronous niques used to implement the inique but requires more hard (b) Parallel Encoder	after another without start / stop bits or (c) both of them he analog to digital converter,
Choose the correct answer: 1- In	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder er circuit used for ADC,	after another without start / stop bits or (c) both of them the analog to digital converter, dware than the other two methods. (c) Serial Encoder when the value of the ramp becomes
Choose the correct answer: 1- In	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder er circuit used for ADC, ue, the binary value of the co	after another without start / stop bits or (c) both of them he analog to digital converter, dware than the other two methods. (c) Serial Encoder when the value of the ramp becomes ounter is read.
Choose the correct answer: 1- In	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder er circuit used for ADC,	after another without start / stop bits or (c) both of them the analog to digital converter,
Choose the correct answer: 1- In	(b) Asynchronous niques used to implement the inique but requires more hard (b) Parallel Encoder or circuit used for ADC, ue, the binary value of the could be and to	after another without start / stop bits or (c) both of them he analog to digital converter,
Choose the correct answer: 1- In trangaps. (a) Synchronous 2- There are three main teel is considered the fast teel (a) Counting Encoder 3- In the counting encode the sample value (a) Greater than	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder or circuit used for ADC, ue, the binary value of the code (b) Equal to	after another without start / stop bits or (c) both of them the analog to digital converter, dware than the other two methods. (c) Serial Encoder when the value of the ramp becomes ounter is read. (c) Less than augh a process called
Choose the correct answer: 1- In	(b) Asynchronous niques used to implement the inique but requires more hard (b) Parallel Encoder or circuit used for ADC, ue, the binary value of the could be and to	after another without start / stop bits or (c) both of them the analog to digital converter,
Choose the correct answer: 1- In trangaps. (a) Synchronous 2- There are three main teel is considered the fast teel (a) Counting Encoder 3- In the counting encode the sample value (a) Greater than 4- Non uniform quantization (a) Expanding	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder er circuit used for ADC, ue, the binary value of the code (b) Equal to n is practically achieved through Companding	after another without start / stop bits or (c) both of them the analog to digital converter, dware than the other two methods. (c) Serial Encoder when the value of the ramp becomes ounter is read. (c) Less than tugh a process called
Choose the correct answer: 1- In	(b) Asynchronous nniques used to implement the inique but requires more hard (b) Parallel Encoder er circuit used for ADC, ue, the binary value of the companding (b) Companding	after another without start / stop bits or (c) both of them he analog to digital converter, dware than the other two methods. (c) Serial Encoder when the value of the ramp becomes ounter is read. (c) Less than hugh a process called
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(a) Bit rate	(b) Pulse shape	(c) Both of them
7- In w	vave modulation some parameter tinuously in accordance with th	ers (amplitude, duration, position) of a carrie e message signal.
(a) Continuous	(b) Pulse	(c) Both of them
		sing a tone modulation message
as a modulation in	dex increases. (b) Decreases	(c) Not affected
(a) Increases	(0) 20010000	
9. There are many	categories of amplitude modu	lation the figure shown below describe the
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		Fr. 31
		The state of the s
	and the second s	
	:[J
(a) DSB-WC	l (b) DSB-SC	(c) SSB

Question Three:

(a) 999000Hz

(20 Marks)

(c) 10⁷ Hz

1- Assume that an analog voice frequency signal, which occupies a band from 300 to 3400 HZ, is to be transmitted over a μ =255 law companded PCM system. Assume that each sample value is represented by 7 information bits plus 1 parity bit. Find the following:

(b) 1001000 Hz

- Number of levels used in quantizer. a)
- Bit rate. b)
- Assume a code word of transmitted data in a certain time is 11001100 represent the c) digital signal using Bipolar (RZ) Signaling.
- Bandwidth using this line code d)
- The output SNR for this companded PCM system e)
- 2- Draw the block diagram of the Pulse code modulation system.

- 1- A Modulating signal m(t) is a pure sinusoidal signal as m(t) =10 cos ($w_m t$), which modulated using AM modulation system corresponding to the modulation index μ =2.
 - (a) Find the amplitude and power of the carrier.
 - (b) Write the expression of the modulated AM signal.
 - (c) Sketch the AM signal.
 - (d) Find the sideband power.
 - (e) Find the power efficiency.
- 2- A single tone FM signal is:

$$\varphi_{\rm FM}(t) = 10 \left[\cos \left(2\pi (10^6) t + 8 \sin(2\pi (10^5) t) \right) \right]$$

Determine the following:

- (a) The carrier frequency f_e.
- (b) The modulation index B.
- (c) The peak frequency deviation Δf .